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سوابق تحصیلی

- ❖ فوق دکتری: دانشگاه کنتاکی آمریکا – ۱۳۹۵-۱۳۹۶
- ❖ دکتری: مهندسی کامپیوتر (گرایش معماری کامپیوتر) از دانشگاه شهید بهشتی تهران - ۱۳۹۴
- ❖ کارشناسی ارشد: مهندسی کامپیوتر (گرایش معماری کامپیوتر) از دانشگاه شهید بهشتی تهران - ۱۳۸۹
- ❖ کارشناسی: مهندسی کامپیوتر (گرایش سخت افزار) از دانشگاه شهید باهنر کرمان - ۱۳۸۶
- ❖ دیپلم: ریاضی فیزیک با معدل ۱۹/۳۰ - ۱۳۸۰

مهارت‌های فنی و تخصصی

- ❖ معماری‌های سخت‌افزاری و مدارهای دیجیتال
- ❖ تحلیل، طراحی و سنتز مدارهای دیجیتال
- ❖ طراحی، پیاده‌سازی، چینش و شبیه‌سازی مدارهای دیجیتال در سطوح معماری، منطق و ترانزیستور
- ❖ ابزارها: Ledit، PSPICE، HSPICE، Cadence

فعالیت‌های پژوهشی

➤ مقالات

Journal Papers

1. H. Thapliyal, F. Sharifi, "Energy Efficient Design of Magnetic Circuits", IEEE Transaction on Magnetic. Accepted.

2. H. Thapliyal, A. Mohammad, S.D. Kumar, F. Sharifi, "Energy-Efficient Magnetic 4-2 Compressor", Microelectronics Journal, vol. 67, pp. 1-9, 2017.

3. F Sharifi, A Panahi, MH Moaiyeri, H Sharifi, K Navi, "High Performance CNFET-based Ternary Full Adders", IETE Journal of Research, 2017.

4. MH Moaiyeri, A Rahi, F Sharifi, K Navi, "Design and evaluation of energy-efficient carbon nanotube FET-based quaternary minimum and maximum circuits", Journal of Applied Research and Technology, 2017.

5. S Tabrizchi, A Panahi, F Sharifi, K Navi, N Bagherzadeh, "A novel method for designing ternary adder cell based on CNFETs", IET Circuits, Devices & Systems, 2017.

6. S Tabrizchi, H Sharifi, F Sharifi, K Navi, "Design of Ultra Low Power Ternary Half Adder and Multiplier for Nanotechnology", Journal of Nanoelectronics and Optoelectronics, Vol. 11, pp. 730-737, 2016.

7. F. Sharifi, A. Panahi, H. Sharifi, K. Navi, N. Bagherzadeh and Himanshu Thapliyal "Design of quaternary 4–2 and 5–2 compressors for nanotechnology " Elsevier, Computers & Electrical Engineering, Vol. 56, pp. 64-74, 2016.

8. A Panahi, F Sharifi, MH Moaiyeri, K Navi, "CNFET-based approximate ternary adders for energy-efficient image processing applications", Microprocessors and Microsystems Vol. 47, pp. 454-465, 2016.

9. MH Moaiyeri, S Sedighiani, F Sharifi, K Navi, "Design and analysis of carbon nanotube FET based quaternary full adders", Frontiers of Information Technology & Electronic Engineering, Vol. 17, pp. 1056-1066, 2016.

10. S Tabrizchi, H Sharifi, F Sharifi, K Navi, "A novel design approach for ternary compressor cells based on CNTFETs", Circuits, Systems, and Signal Processing, Vol.35, pp. 3310-3322, 2016.

11. H. Sharifi, F. Sharifi, A. Belghadr "Low-Power CMOS/Nanomaterial Three-Dimensional Field Programmable Gate Array Architecture", ASP, Quantum Matte, vol. 5, pp. 612-615, 2016.

12. F Sharifi, MH Moaiyeri, K Navi, N Bagherzadeh, "Ultra-Low-Power Carbon Nanotube FET-based Quaternary Logic Gates", International Journal of Electronics, 2016.

13. F. Sharifi, M. H. Moaiyeri, K. Navi, and N.Bagherzadeh, "Robust and Energy-Efficient Carbon Nanotube FET-based MVL Gates; A Novel Design Approach," Elsevier, Microelectronics journal, vol. 46 , pp. 1333-1342, 2015.

14. F. Sharifi, M. H. Moaiyeri, K. Navi, M. A. Tahaerkhani, " Carbon Nanotube-FET Based Decimal Decoder and Multiplexer Circuits" journal of Quantum matter, vol.4 , pp. 565- 569, 2015.

15. F. Sharifi, M. H. Moaiyeri, K. Navi, and N. Bagherzadeh, "Quaternary full adder cells based on carbon nanotube FETs," Springer, Journal of Computational Electronics, vol. 14, pp. 762-772, 2015.

16. F. Sharifi, M. H. Moaiyeri, and K. Navi, "A Novel Quaternary Full Adder Cell Based on Nanotechnology," International Journal of Modern Education and Computer Science (IJMECS), vol. 7, p. 19, 2015.

17. M. H. Moaiyeri, M. Shamohammadi, F. Sharifi, K. Navi, "High-Performance Ternary Logic Gates for Nanoelectronics," International Journal of High Performance Systems Architecture, vol. 5 , pp. 209-215, 2015.

18. E. Alkaldy, K. Navi, and F. Sharifi, "A Novel Design Approach for Multi-input XOR Gate Using Multi-input Majority Function," Arabian Journal for Science and Engineering, vol. 39, pp. 7923-7932, 2014.

19. E. Alkaldy, K. Navi, F. Sharifi, and M. H. Moaiyeri, "An ultra high-speed (4; 2) compressor with a new design approach for nanotechnology based on the multi-input majority function," Journal of Computational and Theoretical Nanoscience, vol. 11, pp. 1691-1696, 2014.

20. A. Rezaei, M. Masoudi, F. Sharifi, and K. Navi, "A Novel High Speed Full Adder Cell based on Carbon Nanotube FET (CNFET)," Int. J. Emerg. Sci, vol. 4, pp. 64-74, 2014.

21. F. Sharifi, A. Momeni, and K. Navi, "CNFET based basic gates and a novel full-adder cell," International

Journal of VLSI Design & Communication Systems, vol. 3, p. 11, 2012.

22. M. Jamalizadeh, F. Sharifi, M. H. Moaiyeri, K. Navi, and O. Hashemipour, "Five new MVL current mode differential absolute value circuits based on carbon nano-tube field effect transistors (CNTFETs)," Nano-Micro Letters, vol. 2, pp. 227-234, 2010.

23. K. Navi, F. Sharifi, A. Momeni, and P. Keshavarzian, "Ultra High Speed CNFET Full- Adder Cell Based on Majority Gates," IEICE transactions on electronics, vol. 93, pp. 932- 934, 2010.

24. K. Navi, A. Momeni, F. Sharifi, and P. Keshavarzian, "Two novel ultra high speed carbon nanotube Full-Adder cells," IEICE Electronics Express, vol. 6, pp. 1395-1401, 2009.

Conference Papers

1. F Sharifi, MZ Saif, AH Badawy, "Design of Adiabatic MTJ-CMOS Hybrid Circuits", MWSCAS, Boston, 2017.

2. F. Sharifi, H. Thapliyal, "Energy-Efficient Magnetic Circuits Based on Nanoelectronic Devices", ISCAS, Baltimore, 2017.

3. F. Sharifi, S. Amanolahi, M. A. Taherkhani, O. Hashemipour"A Flexible Design for Optimization of Hardware Architecture in Distributed Arithmetic based FIR Filters," In IEEE East West Design and Test (EWDTS'12), September 14-17, 2012.

➢ گزارش‌های فنی

1. Research on voltage mode Full Adder under supervision of Professor K. Navi, Department of Electronic and Computer Engineering, Shahid Beheshti University , Tehran, Iran.

2. Research on circuite design based on Carbon Nano Tube Field Effect Transistor under supervision of Professor K. Navi, Department of Electronic and Computer Engineeng, ShahidBeheshti University , Tehran, Iran.

3. Research on micro controlers and designing of fire alarm system under supervision of M. Hosseini Nasab, Department of Computer Engineering, Shahid Bahonar University, Kerman, Iran.

فعالیت‌های آموزشی

➢ تدریس:

❖ طراحی سیستم‌های کامپیوتری امن (دانشگاه تحصیلات تکمیلی صنعتی کرمان)

❖ طراحی مدارهای منطقی (دانشگاه آزاد بوشهر)

❖ معماری کامپیوتر (دانشگاه آزاد بوشهر)

❖ معماری کامپیوتر پیشرفته (آموزشیار)

❖ VLSI پیشرفته (آموزشیار)

❖ آزمایشگاه مدارهای منطقی (شهید بهشتی)

زمینه‌های علمی/ تحقیقاتی / اجرایی مورد علاقه

❖ معماری‌های نوین سخت‌افزاری (NOC ، ASIP)

❖ طراحی مدارات دیجیتال

❖ استفاده از ابزارهای مقیاس نانو در طراحی مدارات

❖ امنیت سخت افزار

❖ CAD (Computer Aided Design)