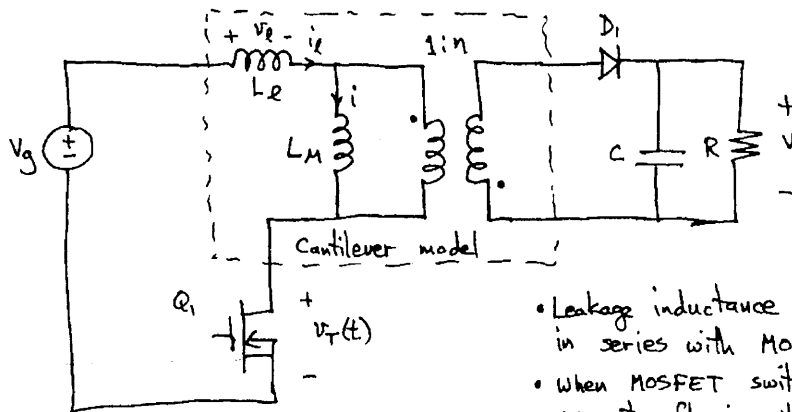


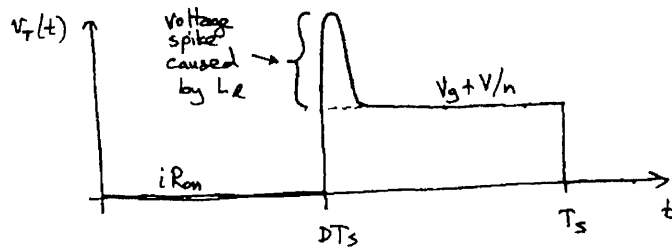
# Effect of transformer leakage inductance

## Voltage clamp snubber



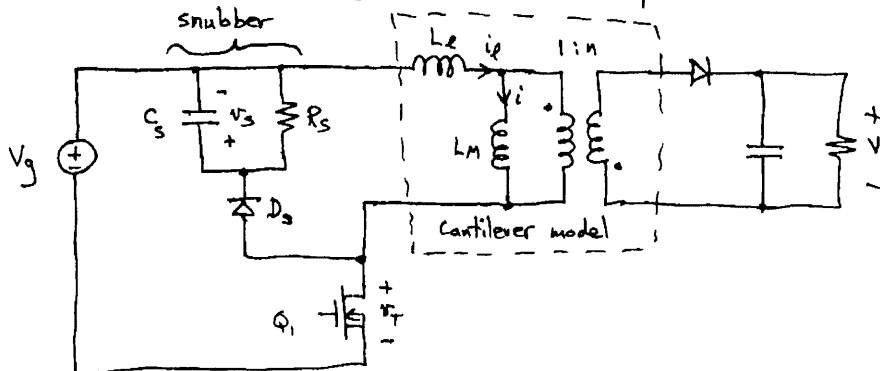
- Leakage inductance  $L_e$  is effectively in series with MOSFET  $Q_1$
- When MOSFET switches off, it interrupts current flowing through  $L_e$
- $L_e$  induces voltage spike according to  $V_r(t) = L_e \frac{di(t)}{dt}$

transistor voltage waveform:

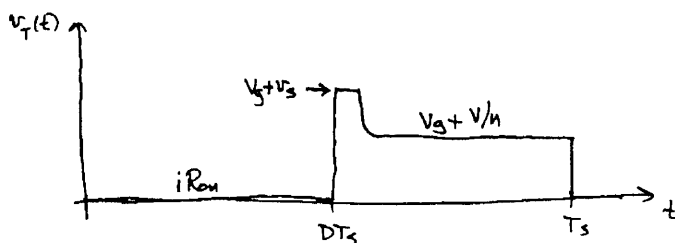


If the peak magnitude of the voltage spike exceeds the voltage rating of the MOSFET, then  $Q_1$  will fail.

## Protection of $Q_1$ using a voltage clamp snubber



- snubber provides a path for  $i_p$  to flow after  $Q_1$  has turned off
- energy stored in  $L_e = \frac{1}{2} L_e i_p^2 = \frac{1}{2} L_e I^2$  is transferred to  $C_s$  and then is dissipated by  $R_s$ . Average power =  $\frac{1}{2} L_e I^2 f_s$
- peak transistor voltage is clamped to  $V_g + v_s > V_g + \frac{V}{n}$



### An approach to select $R_s$ and $C_s$ :

- use large  $C_s$ , so that  $v_s(t)$  has negligible ripple:
 
$$C_s \gg \frac{T_s}{R_s} \Rightarrow v_s(t) \approx V_s$$
- Voltage  $V_s$  rises until power dissipated by  $R_s$  is equal to average power transferred from  $L_e$ :
 
$$\frac{V_s^2}{R_s} = \frac{1}{2} L_e I^2 f_s$$

$\Rightarrow$  choose  $R_s$  such that  $V_s$  is acceptably low
- Note that  $L_e$  depends on winding geometry, and is not known until transformer is wound.
 

$\Rightarrow$  measure  $L_p$  in short circuit test, or guess its value

Example - a first-pass selection of  $R_s$  and  $C_s$

Given  $V_g = 150V$ ,  $V = 15V$ ,  $n = 0.2$   
 $f_s = 100\text{kHz}$ ,  $L_M = 1\text{mH}$   $I = 1.5A$   
MOSFET peak voltage rating =  $400V$   
It is desired to limit peak  $v_T$  to  $325V$

Estimate  $L_e$ : in a good, carefully wound transformer, it may be possible to achieve  $L_e = 3\%$  of  $L_M = 30\mu\text{H}$

Energy stored in  $L_e$  during  $\alpha t < DT_s$ :

$$W_e = \frac{1}{2} L_e I^2 = \left(\frac{1}{2}\right) (30\mu\text{H}) (1.5A)^2 = 33.75\mu\text{J}$$

Average power transferred from  $L_e$  to snubber:

$$P_e = W_e f_s = (33.75\mu\text{J}) (100\text{kHz}) = 3.375\text{W}$$

To limit peak  $v_T$  to  $325V$ , we need

$$V_s = (\text{peak } v_T) - V_g = 325 - 150 = 175V$$

So choose

$$R_s = \frac{V_s^2}{P_e} = \frac{(175)^2}{(3.375\text{W})} = 9074\Omega$$

we might use a  $10\text{k}\Omega$ ,  $5\text{W}$  resistor. Then

$$C_s \gg \frac{T_s}{R_s} = \frac{(10\mu\text{s})}{(10\text{k}\Omega)} = 1\text{nF}$$

A good choice might be  $C_s = 47\text{nF}$ ,  $250V$ .

The above calculations are based on the estimate  $L_e = 3\%$  of  $L_M$ , and should be considered first-pass estimates.